# **Iseg Board Layout Examination**

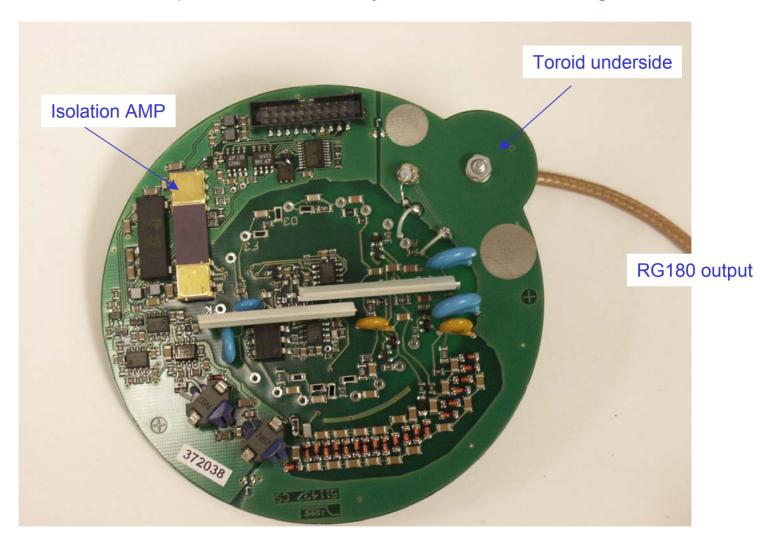
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### **Top View**

The SMT components are located very close to the PMT mounting holes.

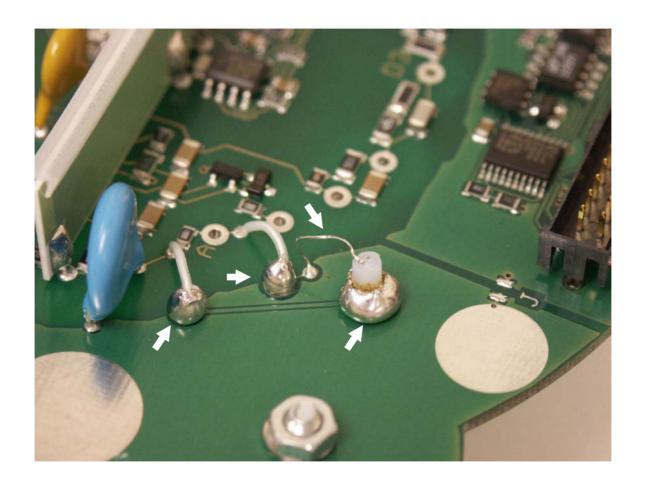


#### **Coaxial Cable Attachment**

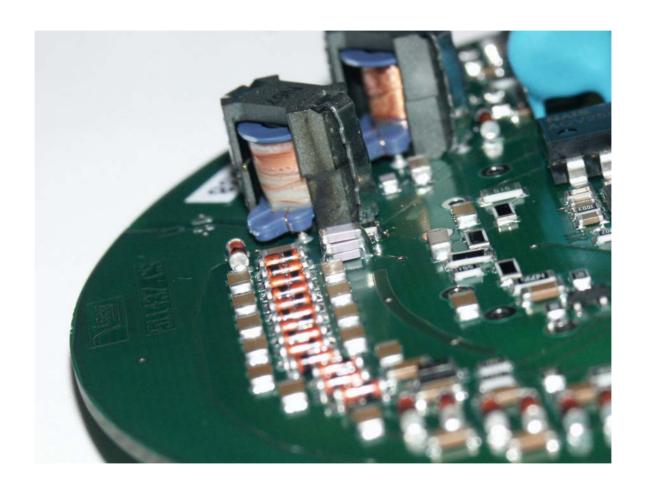
Insufficient shield-to-ground isolation.

No board wash after manual soldering.

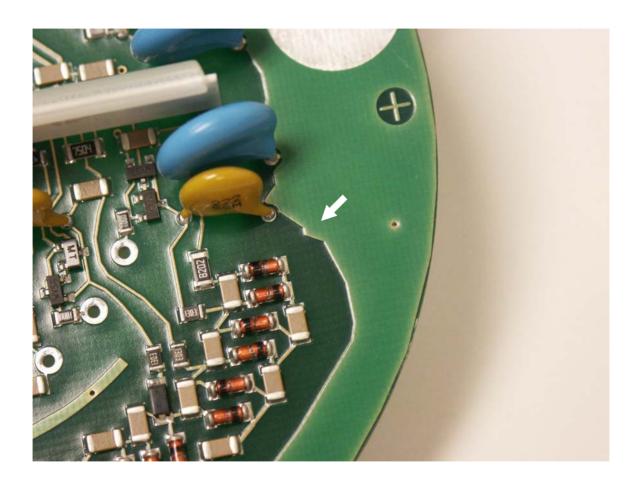
Exposed and twisted center conductor for signal output.



## **Stacked SMT Capacitors**



## **Corona Point Near the Highest-Voltage Traces**



### **Bottom View**

Numerous corona points near the PMT pins (only some of them are marked.)

